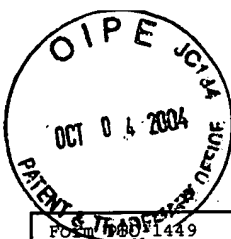


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Form PTO-1449		U.S. Department of Commerce Patent And Trademark Office		ATTY. DOCKET NO. ITW 7188.63		SERIAL NO. 09/827440	
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION				APPLICANT James M. Thommes			
				FILING DATE April 6, 2001		GROUP	
U.S. PATENT DOCUMENTS							
EXAM. INIT.	REF	PATENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
FOREIGN PATENT DOCUMENTS							
EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
		Reduction of Voltage Stress in Integrated High-Quality Rectifier-Regulators by Variable-Frequency Control, M.M. Jovanovic, D.M. Tsang, and F.C. Lee, Proceeding of the Applied Power Electronics Conference, Orlando, FL, Feb. 13-17, 1994					
		Single-Stage Single-Phase Parallel Power Factor Correction Scheme, Y. Jiang and F.C. Lee, Proceedings of the Power Electronics Specialist Conference, Taipei, Taiwan June 20-25, 1994					
		A Novel Single-Phase Power Factor Correction Scheme, Y.M. Jiang, F.C. Lee, G.C. Hua, and W. Tang, Proceedings of the Applied Power Electronics Conference, San Diego, CA 3/7-11/93					
		Boost Power Factor Corrector Design With The UC3853, By Phillip C. Todd, Unitrode Corporation					
		A High Performance Linear Regulator For Low Dropout Applications, Dave Zendzian, Unitrode Corporation					
EXAMINER				DATE CONSIDERED			
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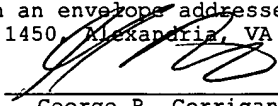


Sheet 1 of 1.

Form 1449		U.S. Department of Commerce Patent And Trademark Office		ATTY. DOCKET NO. ITW 7188.63		SERIAL NO. 09/827440	
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EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO
OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
		Power Factor Correction Using The UC3852 Controlled On-Time Zero Current Switching Technique, Bill Andreyckak, Unitrotode					
		UC3854 Controlled Power Factor Correction Circuit Design, Philip C. Todd, Unitrode					
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